

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	84	((execut\$3 or run\$4 or perform\$3 or carry\$3 or process\$3) near3 (stack and register)).clm.	US-PGPUB	OR	ON	2006/06/21 17:28
L2	747	((logic or instruction or code or algorithm) near3 (execut\$3 or run\$4 or perform\$3 or carry\$3 or process\$3) near3 (register)).clm.	US-PGPUB	OR	ON	2006/06/21 17:28
L3	105	(register near (file or module)) near4 ((logic or instruction or code or algorithm) near3 (execut\$3 or run\$4 or perform\$3 or carry\$3 or process\$3)).clm.	US-PGPUB	OR	ON	2006/06/21 17:29
L4	0	((hardware near accelerator) with stack with logic with virtual with counter with jump with sign with offset with bytecode oerand).clm.	US-PGPUB	OR	ON	2006/06/21 17:29
L5	2	1 and 2 and 3	US-PGPUB	OR	ON	2006/06/21 17:27
L6	0	1 and 2 and 3 and 4	US-PGPUB	OR	ON	2006/06/21 17:27
L7	3785	((execut\$3 or run\$4 or perform\$3 or carry\$3 or process\$3) near3 (stack and register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/21 17:28
L8	15646	((logic or instruction or code or algorithm) near3 (execut\$3 or run\$4 or perform\$3 or carry\$3 or process\$3) near3 (register))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/21 17:28
L9	1743	(register near (file or module)) near4 ((logic or instruction or code or algorithm) near3 (execut\$3 or run\$4 or perform\$3 or carry\$3 or process\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/21 17:29
L10	0	((hardware near accelerator) with stack with logic with virtual with counter with jump with sign with offset with bytecode with operand)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/21 17:30
L11	0	7 and 8 and 9 and 10	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/21 17:30

EAST Search History

L12	178	7 and 8 and 9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/21 17:31
L13	58	12 and (@ad<"20001010" and @rlad<"20001010")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/21 17:31
L14	5	13 and (jump near (subroutine or procedure or routine))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/21 17:33
L15	5	14 and (sign near (exten\$4 or expan\$4 or carr\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/21 17:39
L16	5	15 and counter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/21 17:41
L17	0	16 and wide	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/21 17:42
L18	2170	(717/114-118,139-140,148,165). CCLS.	US-PGPUB; USPAT	OR	OFF	2006/06/21 17:45
L19	1101	18 and (@ad<"20001010" or @rlad<"20001010")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/21 17:46
L20	0	19 and 16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/06/21 17:46



execute logic register-based instructions register file hardware accelerator virtual machine... 1992 - 2000 Search

Adv
Sch
Sch

☒ Search only in Engineering, Computer Science, and Mathematics.

☐ Search in all subject areas.

Scholar Results 1 - 1 of 1 for execute logic register-based instructions register file hardware accelerator

The Delft-Java Engine - group of 8 »

All articles Recent articles

CJ Glossner, S Vassiliadis - Lecture Notes in Computer Science, 1997 -
library.tudelft.nl

... 3.8 D ELFT -J AVA Composite **Logic** Operations. ... 4.5 Final D ELFT -J AVA **Instructions**. ...

usage of such a lan- guage is its "write once, run anywhere" philosophy ...

Cited by 2 - View as HTML - Web Search - BL Direct

execute logic register-based instructions register file hardware accelerator virtual machine... Search

Google Home - About Google - About Google Scholar

©2006 Google

Interference Search

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	84	((execut\$3 or run\$4 or perform\$3 or carry\$3 or process\$3) near3 (stack and register)).clm.	US-PGPUB	OR	ON	2006/06/21 17:19
L2	747	((logic or instruction or code or algorithm) near3 (execut\$3 or run\$4 or perform\$3 or carry\$3 or process\$3) near3 (register)).clm.	US-PGPUB	OR	ON	2006/06/21 17:21
L3	105	(register near (file or module)) near4 ((logic or instruction or code or algorithm) near3 (execut\$3 or run\$4 or perform\$3 or carry\$3 or process\$3)).clm.	US-PGPUB	OR	ON	2006/06/21 17:23
L4	0	((hardware near accelerator) with stack with logic with virtual with counter with jump with sign with offset with bytecode oerand).clm.	US-PGPUB	OR	ON	2006/06/21 17:25
L5	2	1 and 2 and 3	US-PGPUB	OR	ON	2006/06/21 17:27
L6	0	1 and 2 and 3 and 4	US-PGPUB	OR	ON	2006/06/21 17:27